| **Equivalence Class Design** | | | | | | | | | |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| **Valid EC** |  |  |  |  | | **Invalid EC** | |  |  |
| **EC ID** | **Condition** | | | |  | **EC ID** | **Condition** | | |
| EC\_01 | {1000 <= a < 5000} | | | |  | EC\_04 | {a < 1000} | | |
| EC\_02 | {300 <= b < = 800} | | | |  | EC\_05 | {a > 5000} | | |
| EC\_03 | {500 <= c <= 3000} | | | |  | EC\_06 | {b < 600} | | |
|  |  | | | |  | EC\_07 | {b > 800} | | |
|  |  | | | |  | EC\_08 | {c < 500} | | |
|  |  | | | |  | EC\_09 | {c > 3000} | | |
|  |  | | | |  |  |  | | |
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| **Test Case Design and Test Results** | | | | | | | | | | | | | |
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| **Project Name:** | Income | | | | | | | **Project ID:** | |  | | | |
| **Test Strategy:** | Strong Robust Equivalence Class Testing | | | | | | | **Designer:** | |  | | | |
| **Test Environment:** | |  | | | | | | | | | | | |
|  |  | | | |  |  |  |  | |  |  | | |
| **Test Case ID** | **Input** | | | **Expected Result** | | | | | **Actual Result** | | | **Status (Pass/Fail/No run)** |
| IMPELLER | MOTOR | COVER |
| TC01 | 999 | 300 | 500 | Invalid | | | | | Invalid | | | Pass |
| TC02 | 1000 | 299 | 500 | Invalid | | | | | Invalid | | | Pass |
| TC03 | 1000 | 300 | 499 | Invalid | | | | | Invalid | | | Pass |
| TC04 | 1000 | 300 | 500 | 29000 | | | | | 29000 | | | Pass |
| TC05 | 999 | 299 | 500 | Invalid | | | | | Invalid | | | Pass |
| TC06 | 999 | 300 | 499 | Invalid | | | | | Invalid | | | Pass |
| TC07 | 1000 | 299 | 499 | Invalid | | | | | Invalid | | | Pass |
| TC08 | 5000 | 800 | 3000 | 134000 | | | | | 134000 | | | Pass |
| TC09 | 5001 | 800 | 3000 | Invalid | | | | | 134010 | | | Fail |
| **Test Case ID** | **Input** | | | **Expected Result** | | | | | **Actual Result** | | | **Status (Pass/Fail/No run)** |
|  |  |  |
| TC10 | 5000 | 801 | 3000 | Invalid | | | | | 134030 | | | Fail |
| TC11 | 5000 | 800 | 3001 | Invalid | | | | | 134020 | | | Fail |
| TC12 | 5001 | 801 | 3000 | Invalid | | | | | 134040 | | | Fail |
| TC13 | 5001 | 800 | 3001 | Invalid | | | | | 134030 | | | Fail |
| TC14 | 5000 | 801 | 3001 | Invalid | | | | | 134050 | | | Fail |
| TC15 | 5001 | 801 | 3001 | Invalid | | | | | 134060 | | | Fail |
| TC16 | 999 | 299 | 499 | Invalid | | | | | Invalid | | | Pass |
| TC17 | 2000 | 400 | 1000 | 52000 | | | | | 52000 | | | Pass |
| TC18 | 3000 | 700 | 2500 | 101000 | | | | | 101000 | | | Pass |

| **Scenario ID** | **Scenario Name** | **Test Case#** | **Pass** | **Fail** | **No run** | **Block** | **Remark** | **Defect ID** |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
| EC-Lab5-01 | Income | 18 | 11 | 7 | 0 | 0 |  | DF\_TC09 - DF\_TC15 |
| **รวม** | | 18 | 11 | 7 | 0 | 0 |  |  |